GT24C32D

that it received the eight bits of data.

The GT24C32D then prepares for a Read or Write operation by monitoring the bus.

Figure 5. Slave Address

5.9 Write Ope 0.7.14 /F10 G -0.0433 Tc[(5.)]d841.92 re-3(8871 0 595.32 841.92 re W* n BT /F1 9.96 Tf 1 0 0 1 103.1 6

GT24C32D

GT24C32D

GT24C32D

GT24C32D

9.4 UDFN

8L 2x3mm UDFN Package Outline